



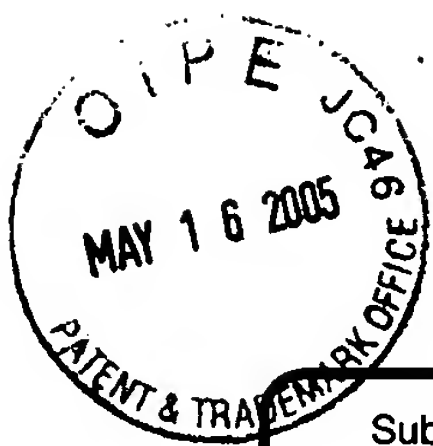
PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, P.O. Box 1450, Alexandria, Virginia, 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia, 22313-1450.**



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

2

of

2

Complete if Known

| | |
|-------------------------|--------------------|
| Application / Conf. No. | 10/659,978 / 5060 |
| Filing Date | September 11, 2003 |
| First Named Inventor | Eric D. Groen |
| Art Unit | 2817 |
| Examiner Name | Arnold M. Kinkead |
| Attorney Docket Number | X-1433 US |

OTHER – NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|------------------------|--------------------------|---|----------------|
| | | John D. van der Tang et al.; "A 9.8-11.5 GHz Quadrature Ring Oscillator for Optical Receivers"; 2002 IEEE; IEEE Journal of Solid-State Circuits, Vol. 37, No. 3; March 2002; pp. 438-442. | |
| | | Seong-Jun Song et al.; "A 4-Gb/s CMOS Clock and Data Recovery Circuit Using 1/8-Rate Clock Technique"; 2003 IEEE; IEEE Journal of Solid-State Circuits, Vol. 38, No. 7; July 2003; pp. 1213-1219. | |
| | | Werner Baumberger; "A Single Chip Image Rejecting Downconverter for the 2.44 GHz Band"; Gallium Arsenide Integrated Circuit (GAASIC) Symposium; 1993; 15th Annual; 10-13 October 1993; pp. 37-40. | |
| | | A Worapishet et al.; "An NMOS Inductive Loading Technique for Extended Operating Frequency CMOS Ring Oscillators"; The 2002 45th Midwest Symposium on Circuits and Systems; Conference Proceedings; August 4-7, 2002; IEEE, Vol. 1 of 3; 4 August 2002; pp. I-116 to I-119. | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |

Examiner
Signature

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.